L Number	Hits	Search Text	DB	Time stamp
13	4919	(714/702,718,722,730,738,739 711/219,200).ccls.	USPAT;	2004/09/03 18:24
İ			US-PGPUB;	
		·	EPO; JPO;	
	İ		DERWENT;	
		(((address near2 (generat\$3 count\$3)) and (off\$1set near2	IBM_TDB USPAT;	2004/09/03 11:40
_	29	register)).ab. and (select\$3 mux multiplex\$3) and (adder	US-PGPUB;	200 1100/00 11110
		addition))	EPO; JPO;	
		addition))	DERWENT;	
			IBM_TDB	
-	32	(((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 11:42
		register)).ab. and (select\$3 mux multiplex\$3) and (adder	US-PGPUB;	
		addition adding))	EPO; JPO;	
			DERWENT; IBM TDB	
	0.5	(/-dd neer) (generat©2 count©2)) and (off©1set near)	USPAT;	2004/09/03 13:10
-	85	((address near2 (generat\$3 count\$3)) and (off\$1set near2	US-PGPUB;	2004/03/00 10:10
		register)).ab.	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	32	(((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 12:53
		register)).ab. ) and (select\$3 mux multiplex\$3) and (adder	US-PGPUB;	
		addition adding)	EPO; JPO;	
			DERWENT; IBM_TDB	
	20	(((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 13:10
-	33	register)).ab.) and (select\$3 mux multiplex\$3) and (adder	US-PGPUB;	200 1700700 10110
	i	addition adding ALU)	EPO; JPO;	
		addition adding / (20)	DERWENT;	
	•		IBM_TDB	
<del>-</del>	1	((((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 12:03
		register)).ab.) and (select\$3 mux multiplex\$3) and (adder	US-PGPUB;	
		addition adding ALU)) not ((((address near2 (generat\$3	EPO; JPO;	
		count\$3)) and (off\$1set near2 register)).ab.) and (select\$3	DERWENT; IBM_TDB	
	1	mux multiplex\$3) and (adder addition adding)) JP-04192046-\$.did.	USPAT;	2004/09/03 12:05
-	1	JF-04 192040-φ.did.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		·	IBM_TDB	
-	42	(((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 12:54
		register)).ab. ) and (select\$3 mux multiplex\$3)	US-PGPUB;	
		·	EPO; JPO; DERWENT;	
			IBM_TDB	
	9	((((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 12:54
		register)) ab. ) and (select\$3 mux multiplex\$3)) not ((((address	US-PGPUB;	
		near2 (generat\$3 count\$3)) and (off\$1set near2 register)) ab. )	EPO; JPO;	
-		and (select\$3 mux multiplex\$3) and (adder addition adding	DERWENT;	
		ALU))	IBM_TDB	0004/00/00 40 40
-	1760	((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 13:10
		register))	US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	1616	(((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 13:11
	1010	register)) ) and (select\$3 mux multiplex\$3) and (adder addition	US-PGPUB;	
		adding ALU)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	2004/00/00 40 4
-	0	((((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 13:11
		register)) ) and (select\$3 mux multiplex\$3) and (adder addition	US-PGPUB; EPO; JPO;	1
		adding ALU)) and programmable near3 inverter	DERWENT;	
	1		IBM_TDB	

_	1595	((((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 13:12
		register)) ) and (select\$3 mux multiplex\$3) and (adder addition	US-PGPUB;	
		adding ALU)) and (memory SRAM)	EPO; JPO;	
			DERWENT;	·
			IBM_TDB	
-	132	(((((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 13:13
		register)) ) and (select\$3 mux multiplex\$3) and (adder addition	US-PGPUB;	
		adding ALU)) and (memory SRAM)) and (((test near3 device)	EPO; JPO;	
	İ	tester ATE) same control\$4)	DERWENT;	
			IBM_TDB	2004/00/02 42:20
-	131	((((((address near2 (generat\$3 count\$3)) and (off\$1set near2	USPAT;	2004/09/03 13:20
	-	register)) ) and (select\$3 mux multiplex\$3) and (adder addition	US-PGPUB;	
		adding ALU)) and (memory SRAM)) and (((test near3 device)	EPO; JPO; DERWENT;	
		tester ATE) same control\$4)) not (((address near2 (generat\$3	IBM TDB	
		count\$3)) and (off\$1set near2 register)).ab.)	USPAT:	2004/09/03 13:20
-	42	((((((((address near2 (generat\$3 count\$3)) and (off\$1set near2	US-PGPUB:	2004/05/05 10:20
		register)) ) and (select\$3 mux multiplex\$3) and (adder addition adding ALU)) and (memory SRAM)) and (((test near3 device)	EPO; JPO;	
		tester ATE) same control\$4)) not (((address near2 (generat\$3	DERWENT;	
		count\$3)) and (off\$1set near2 register)).ab. )) and	IBM TDB	
		(address).ab.		